

Track 1 Summary

- IPL is making real progress
- IPL is focused
- IPL needs your help

For Up To Date Information

IPLNow

IPL Interoperable PCell Libraries NOW.COM

Interoperable PCell Libraries are Here, Now.

Download IPL Library →

Join IPL →

http://www.iplnow.com

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20th EDA Interoperability Developers' Forum
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Open Access

Companies announced an alliance to collaborate on the creation of an Interoperable PCell Library (IPL) that supports the OpenAccess database from the Silicon Integration Initiative (SI2), for the first time in semiconductor industry history, IC designers will be able to use the same PCell libraries in tools from multiple vendors and tools they developed in-house.

Why is it important?

An IPL has many advantages to semiconductor companies, foundries and EDA vendors. Semiconductor companies will be able to use one common PCell library providing advanced functionality across multiple processes and reducing development and support costs while increasing layout flexibility. Foundries will be able to reduce their PDK development costs while dramatically expanding the number of tools they support. EDA vendors will also be able to reduce PDK development costs, while supporting a wider range of foundry partners.

Who are we?

The five founding members of IPL initiative are AWR, Ciranova, Silicon Canvas, Silicon Navigator and Synopsys. All interested foundries, EDA vendors and semiconductor companies are invited to [join](#) the IPL initiative.

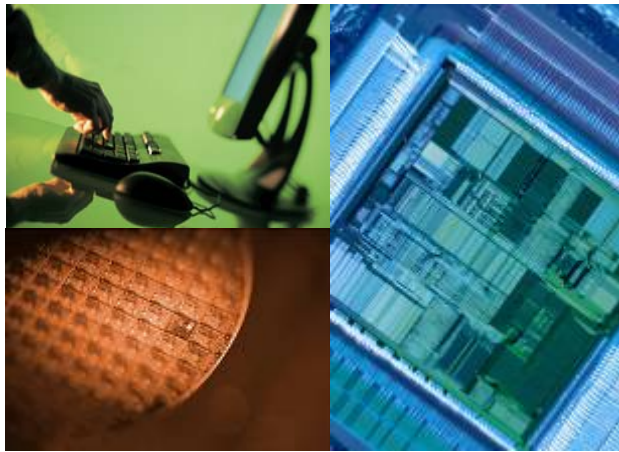
Is this REAL?

IPL members have produced a high-quality proof-of-concept library using a generic 0.13um process. The purpose of this library is to demonstrate that the IPL mechanism works, and to provide a starting point for other companies to start building their own interoperable libraries. The proof-of-concept library is available for [free download](#).

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There's more...



Tell us what you think

- Please complete your feedback form

Mark Your Calendar

- 21st EDA Interoperability Developers' Forum
- April 2008
- In Silicon Valley

Download Presentations from:

www.synopsys.com/devforum/oct2007/presentations

Coming Up Next!

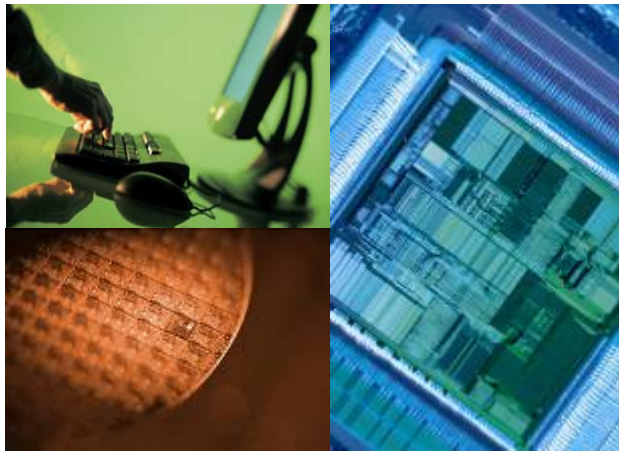
- Interoperability of Analog/Full-Custom Flows *This morning*
- Integrating with VMM Methodology for SystemVerilog *This morning*
- • Lunch featuring *IPL Member Showcase*
- General Session *12:15PM – 2:15PM*
 - Keynote Presentation *The Low Power Revolution*
Michael Keating, Synopsys Fellow and Co-author of “LPMM
 - Introducing the SOI Industry Consortium
 - Liberty UPF/Low Power Checklist
 - Liberty Technical Advisory Board Updates
- The Power of One: UPF on the Path to IEEE Ratification *2:15PM – 4:30PM*
 - Getting Started with UPF/P1801
 - “Keeping Locals Local” by NXP Semiconductors
 - UPF Support Blitz

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