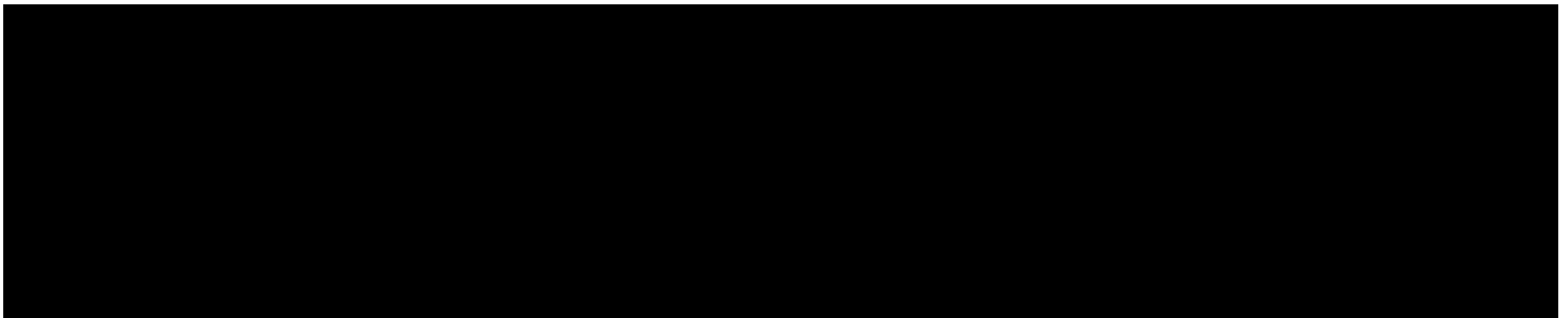


# UPF Means Designers...



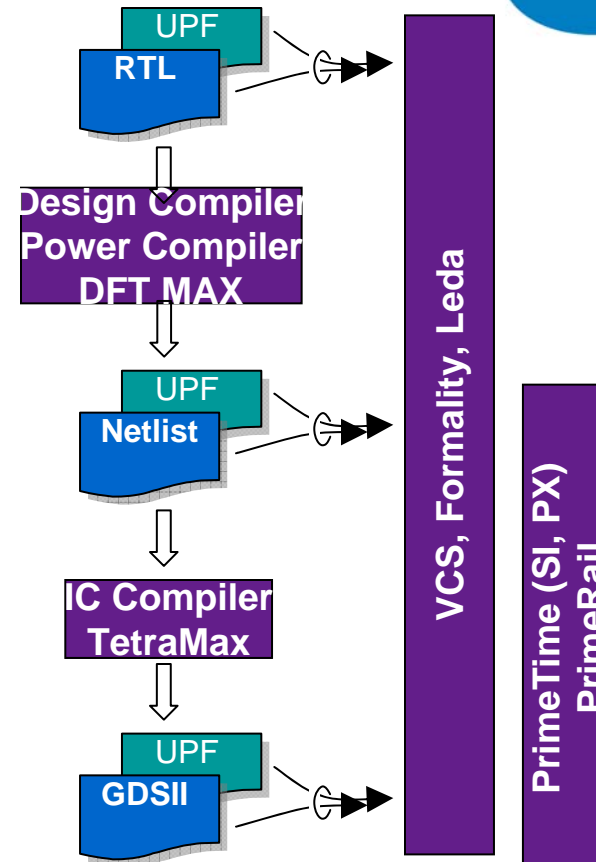
- Get an advanced, integrated low power flow and methodology
  - OK – a number of them have this already
- Get industry interoperability for their low power flows
  - Interoperability requires broad industry support



# Synopsys UPF Support



- Based on existing, proven capabilities
  - 2 years prior experience
  - Over 20 Multi-voltage tapeouts
  - Numerous power-gating tapeouts
- Broad Product Support in 2007
  - Verification
  - Synthesis
  - Test
  - Physical Implementation
  - Checking
  - Signoff
  - Low Power IP





## The Future:

- All EDA companies move to UPF
  - Join the IEEE study/working group
- New versions of UPF tackle system-level tradeoffs and power design intent