

# 19<sup>th</sup> EDA Interoperability Developers Forum

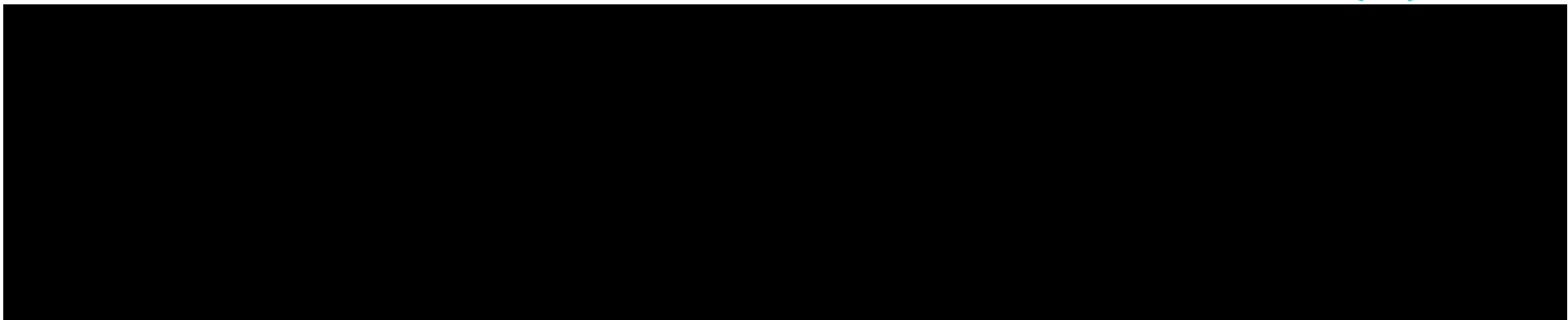


## Panel: Scaling the Power Wall

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# Why Low Power Standard?

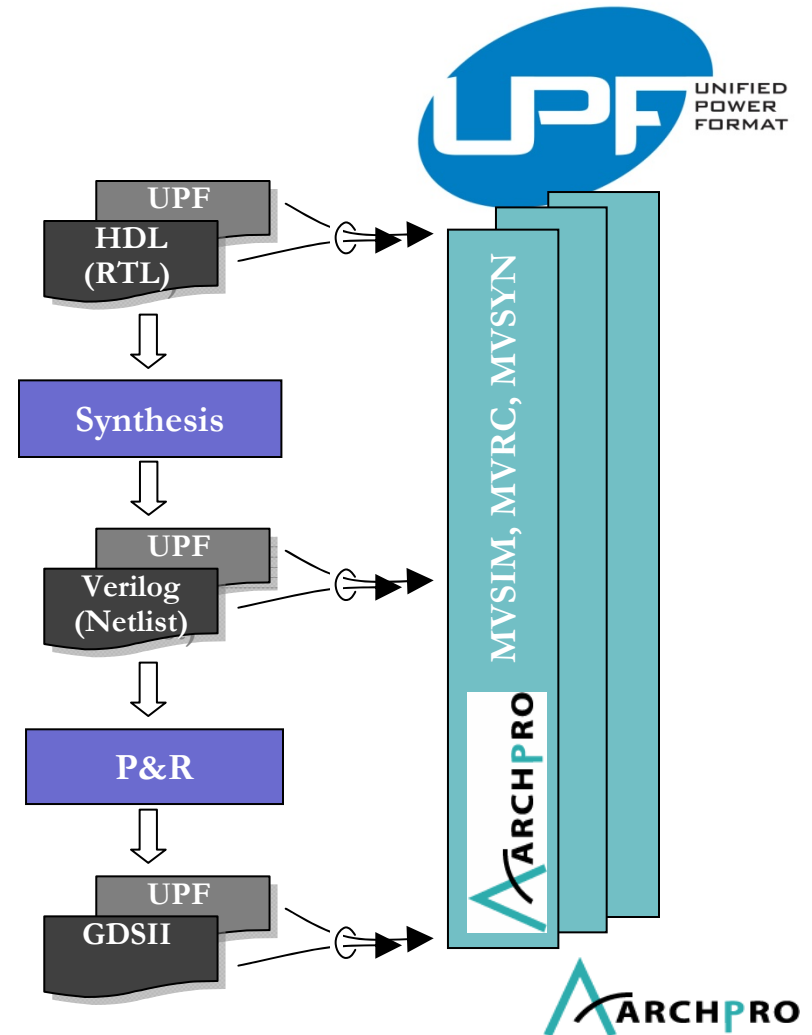


- ArchPro profile:
  - Industry pioneer in power management EDA
  - 45+ team with power management expertise
  - Partners : ARM, Synopsys, Mentor Graphics
- The whole low power design area is dynamic (no pun intended)
  - >85% surveyed 6 months ago using adhoc or proprietary power format
  - Recently more customers are requesting standard formats
  - Customers are looking for:
    - Clarity (in semantics)
    - Consistency (across the flow)
    - Comprehensive (support of LP techniques)



# ArchPro Support of UPF

- Comprehensive MV verification based on UPF
- Read-only support of UPF by DAC 07 (June 2007)
- Read/write support – Q4 07



# Future of UPF : Adoption



- Education
  - Designers still don't realize importance of structured low power methodology
  - Designer-designer training
  - Energy
    - Power reduction technique coverage
- Environment
  - Tool support by vendors (more than just scripts)
  - Rich set of realistic use cases
- Expense of migration

