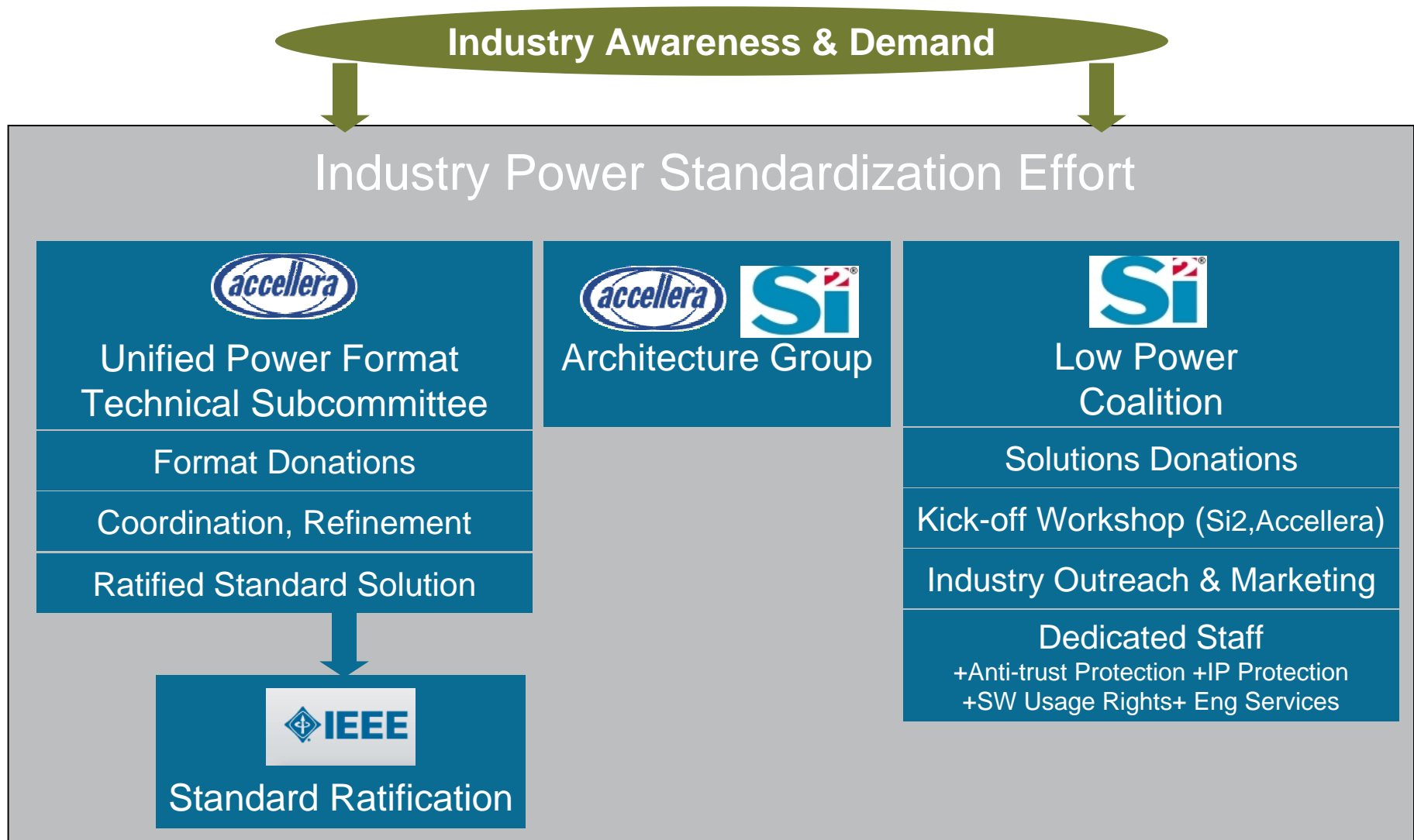


Multiple Pieces Needed

- Library Modeling => Liberty TAB
- Activity Information => SAIF (Accellera) & VCD (IEEE)
- RTL Specification
 - SystemVerilog => Accellera/IEEE
 - VHDL => Accellera/IEEE
- Side file
 - Leverage Si2/Accellera process

Process is Already Underway !



What really matters in standards?

- Practical Usage
 - Is it now, or will it be in use by a “critical mass” of users
 - Which depends on:
- In the early stages - how easy is it to:
 - Read - Get access to the standard ?
 - Use – Use the standard ?
 - Change/Write – Make useful changes to the standard ?

Key Dimensions of Openness

- Access (Read)
 - Requires NDA ?
 - Click-through / web access vs. signed / paper ?
 - Spec and/or code ?
- Use Rights (Use)
 - Includes copyright, patent rights ?
 - Allows use in commercial products ?
 - Other use restrictions ?
 - Can create derivative works ?
- Control of Standard (Write/Change)
 - Can create derivative works ?
 - Multi-company, open steering group ?
 - No one with veto rights ?
 - Anybody can work on standard, vote ?