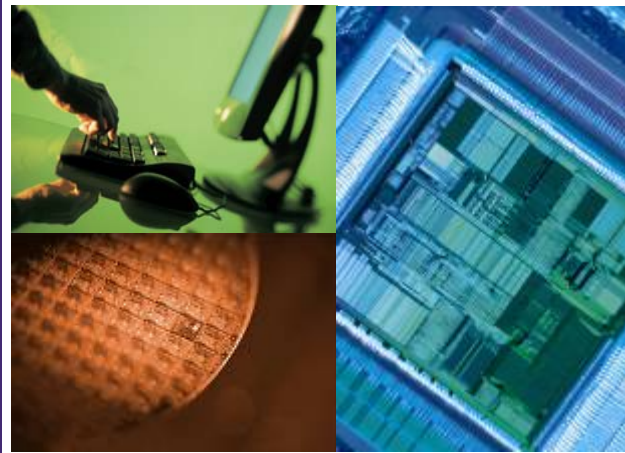
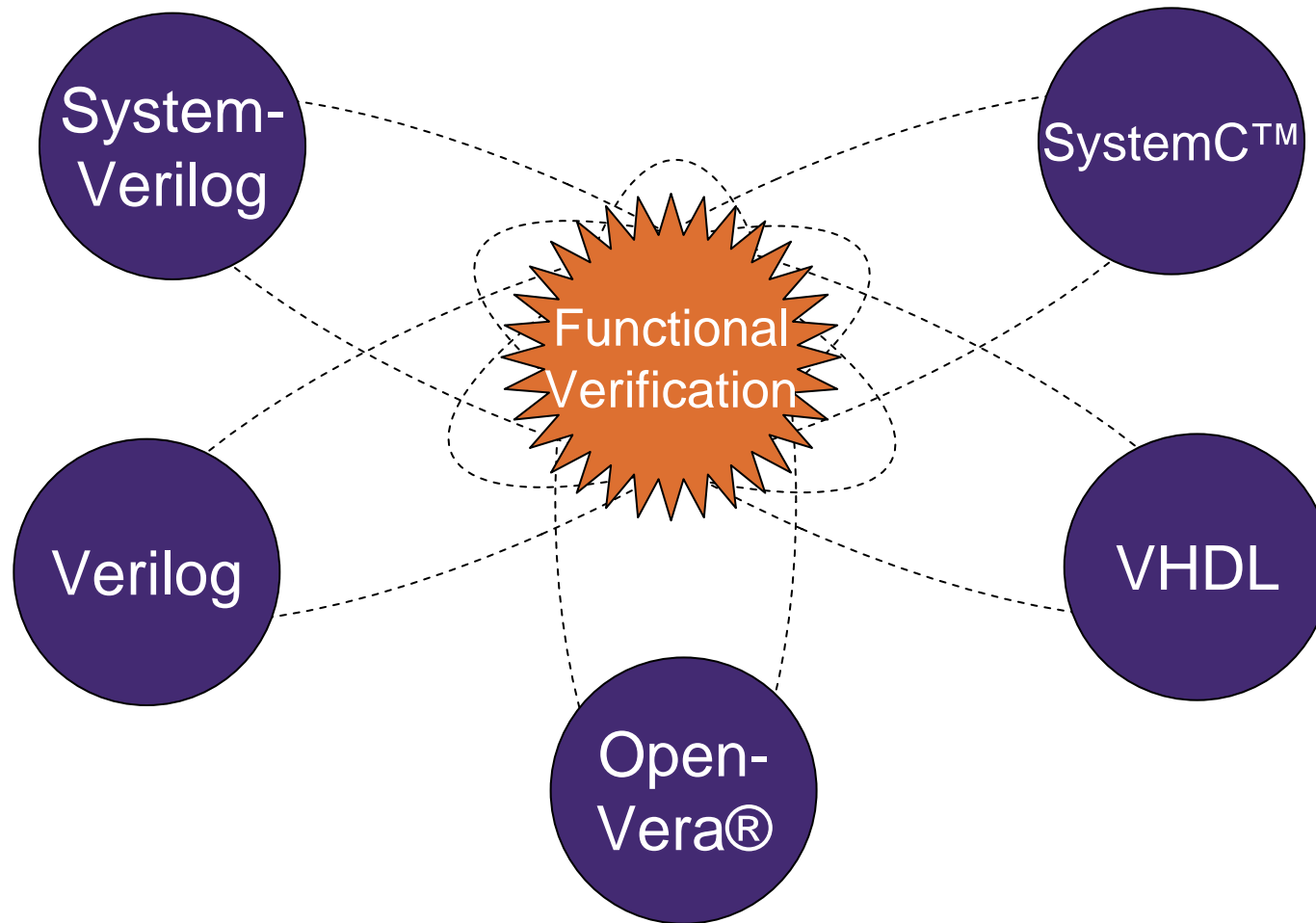


# Organizing the Verification Solar System

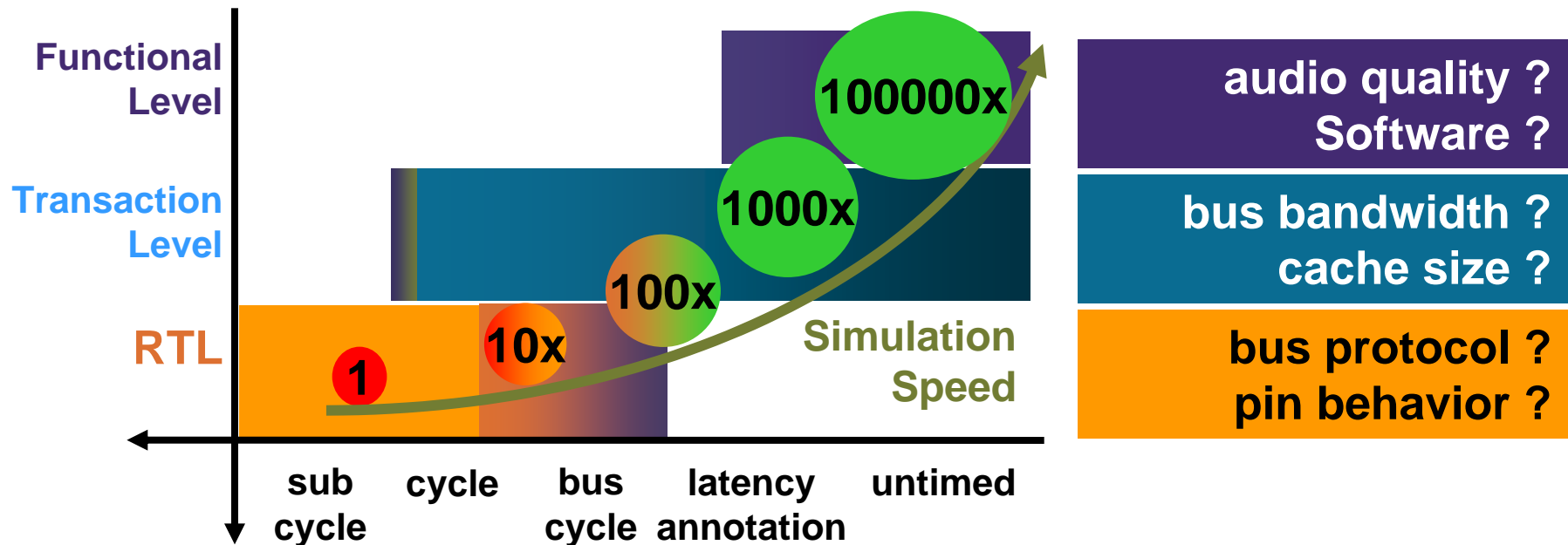
Janick Bergeron  
Scientist



# Heavenly Bodies

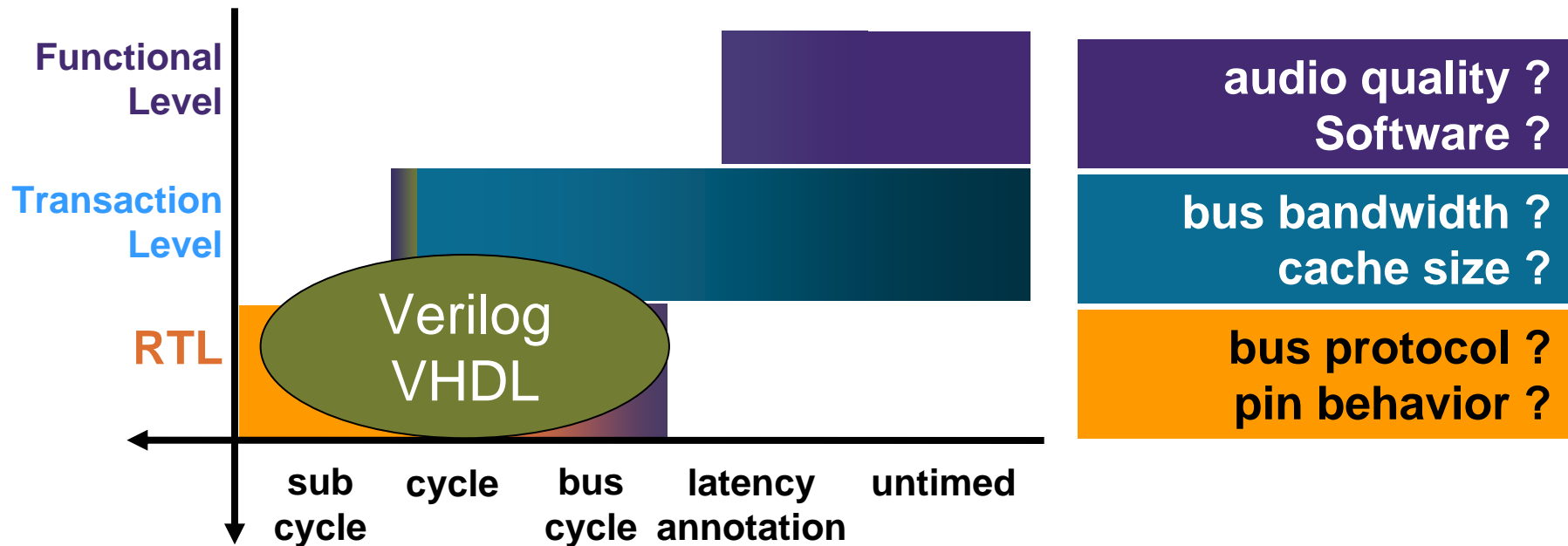


# Abstraction Levels

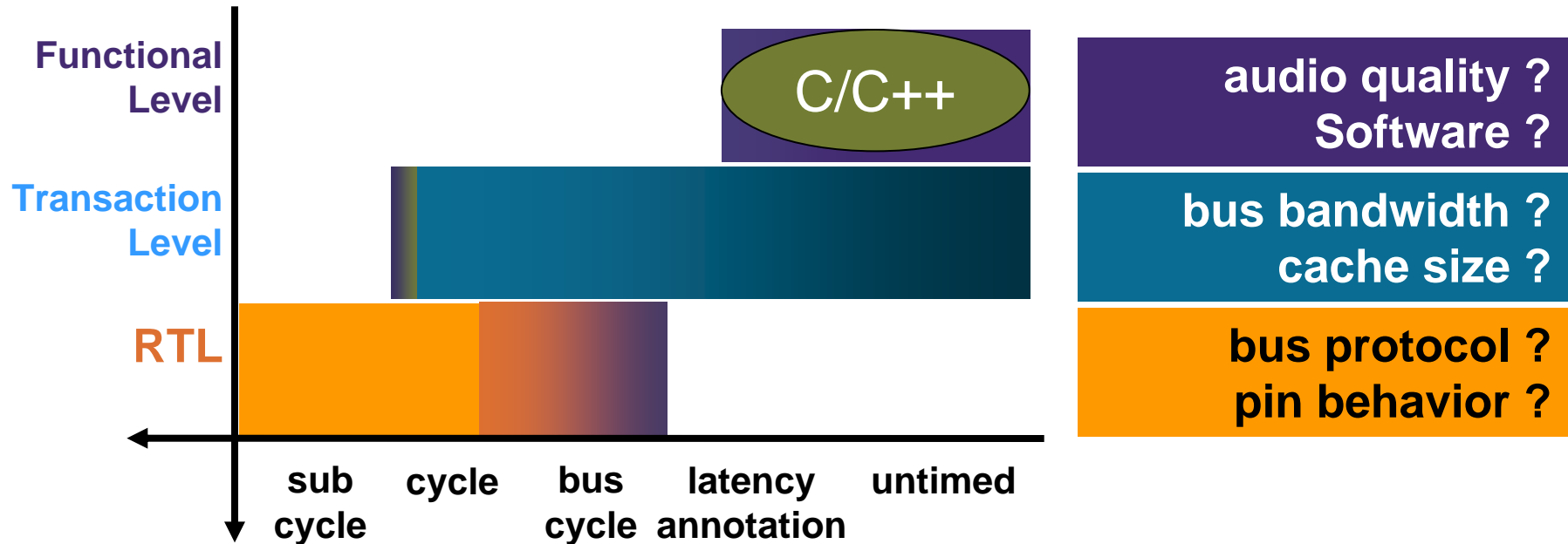


**Solve problems at the appropriate abstraction levels, minimizing effort ⇔ cpu time.**

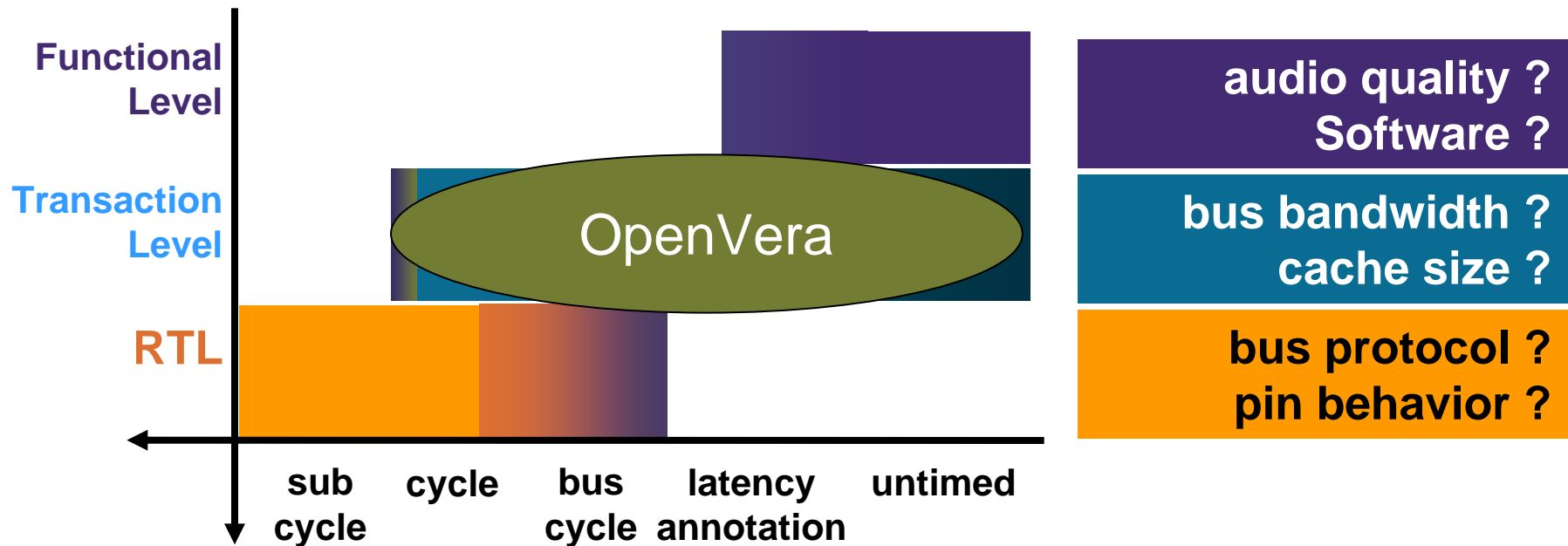
# Languages



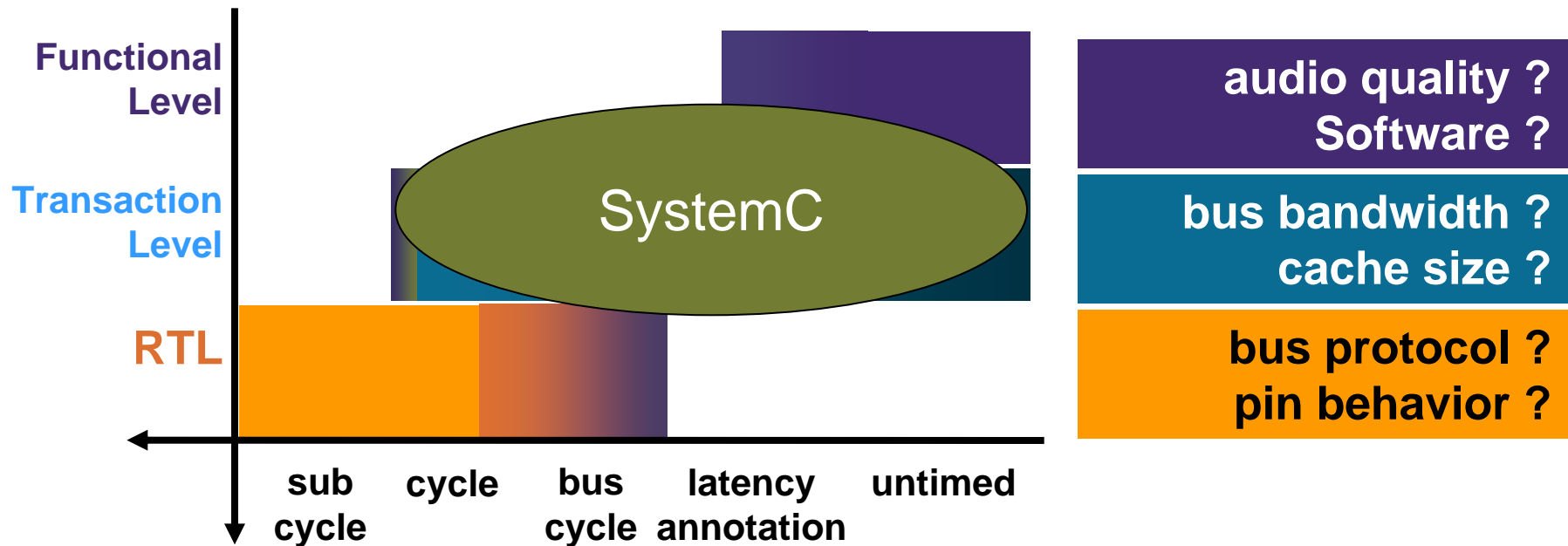
# Languages



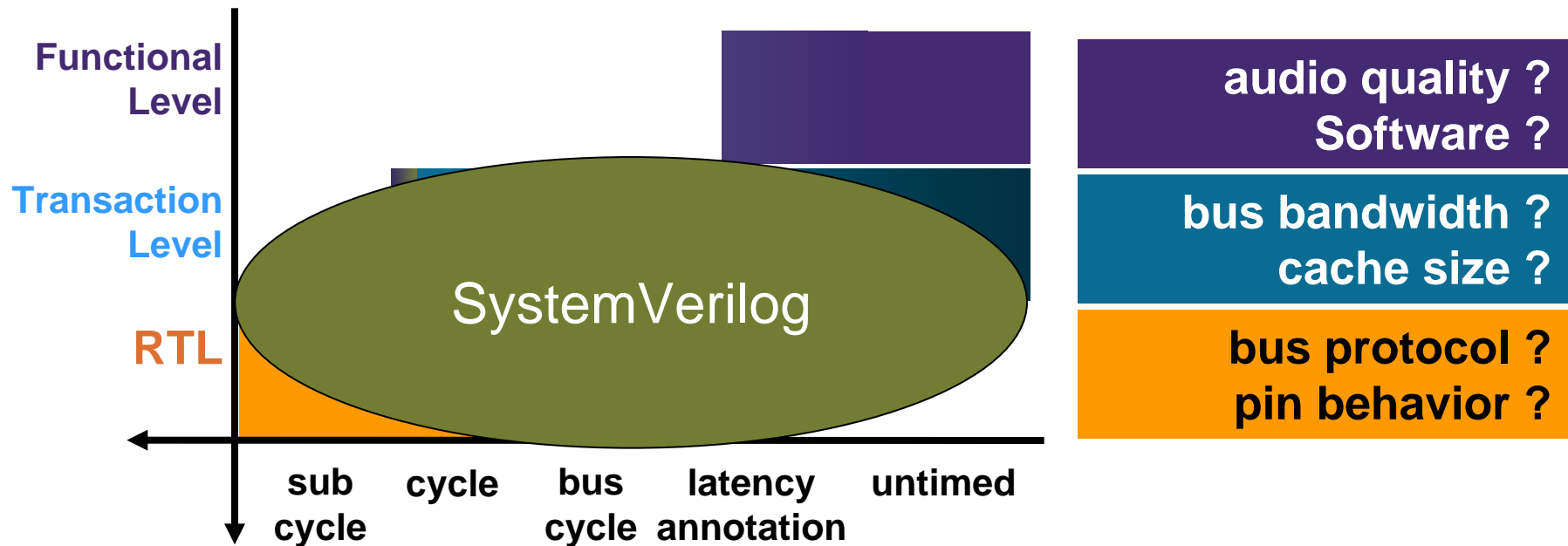
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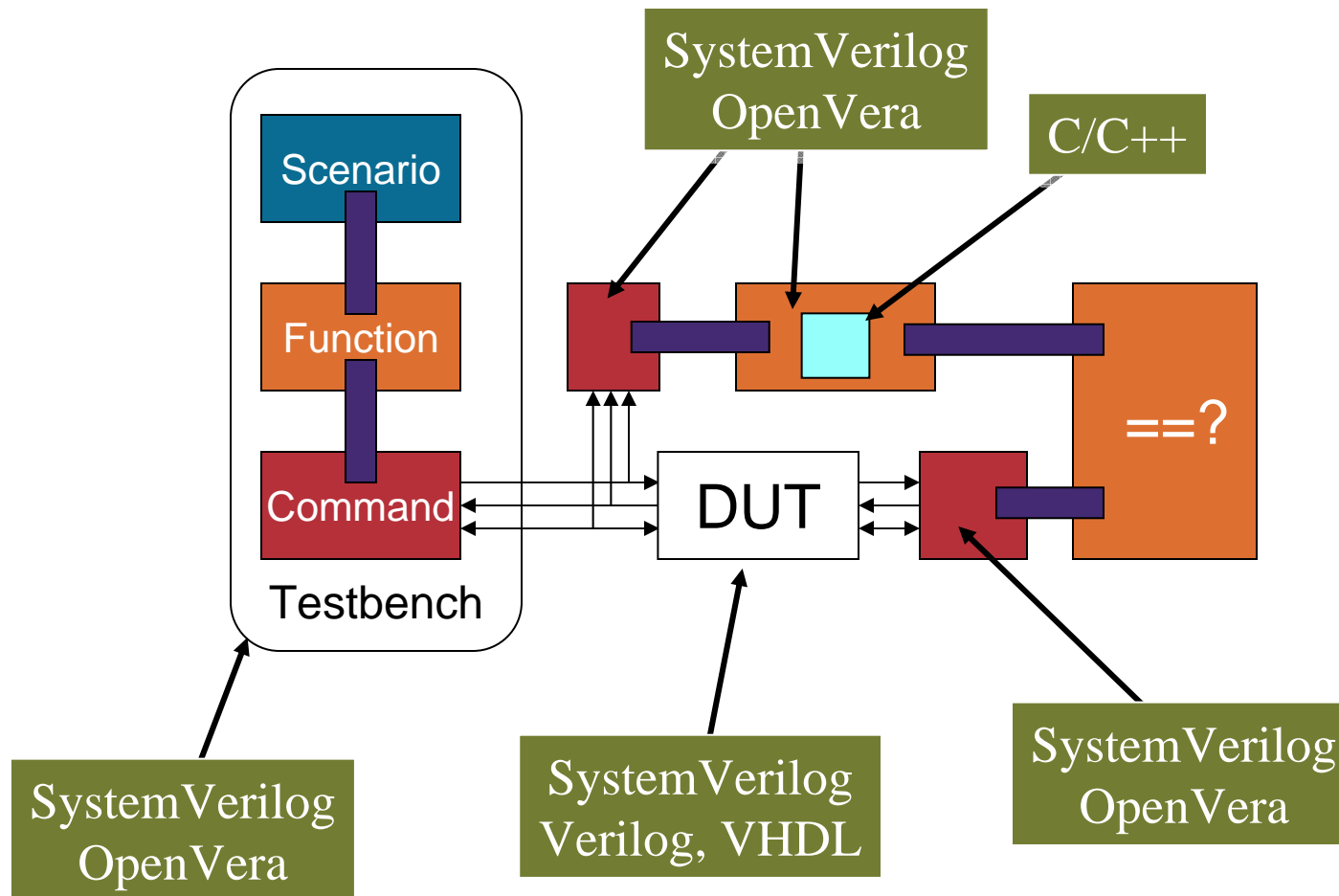
# Languages



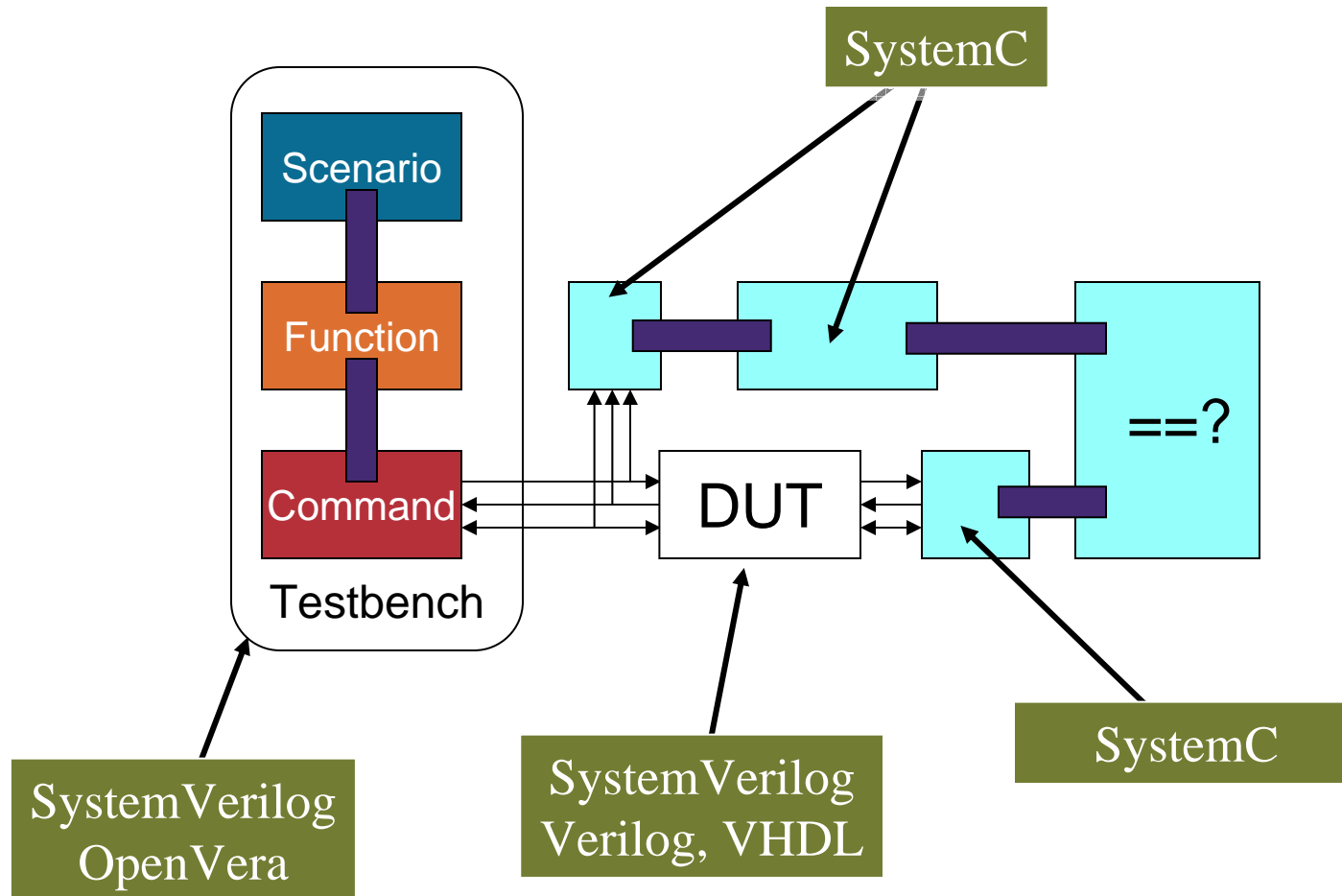
# Languages



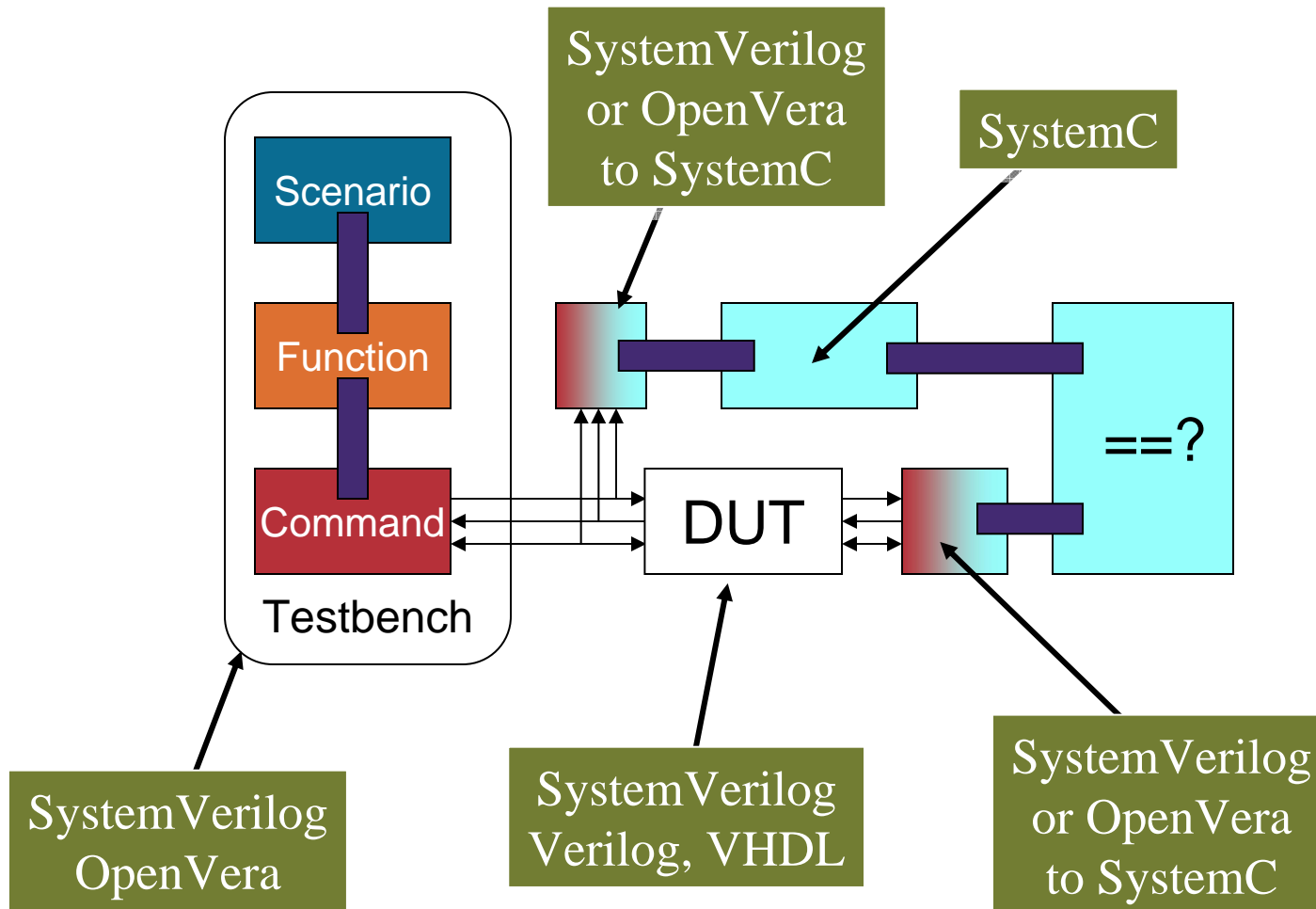
# Verification Environments



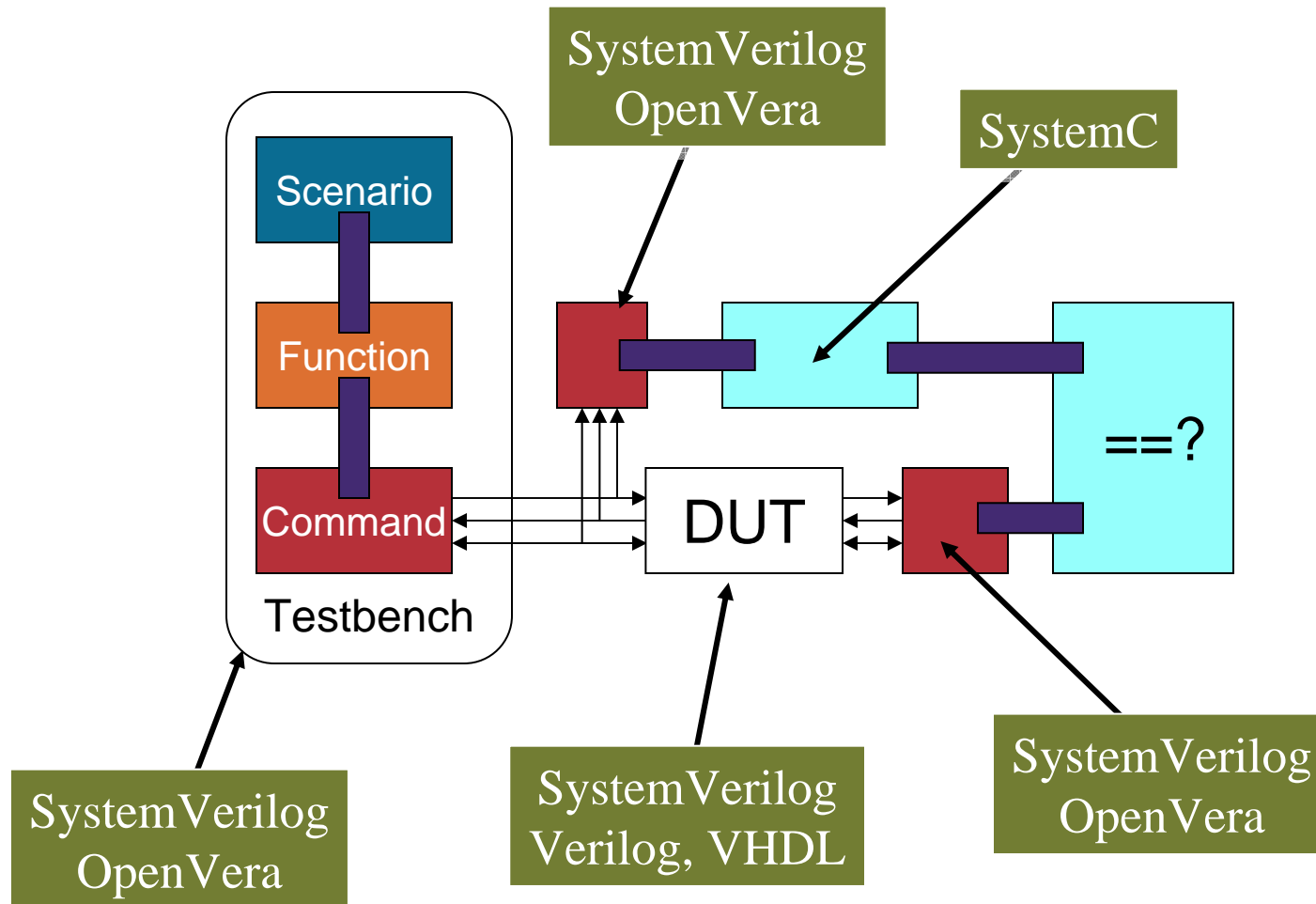
# Verification Environments



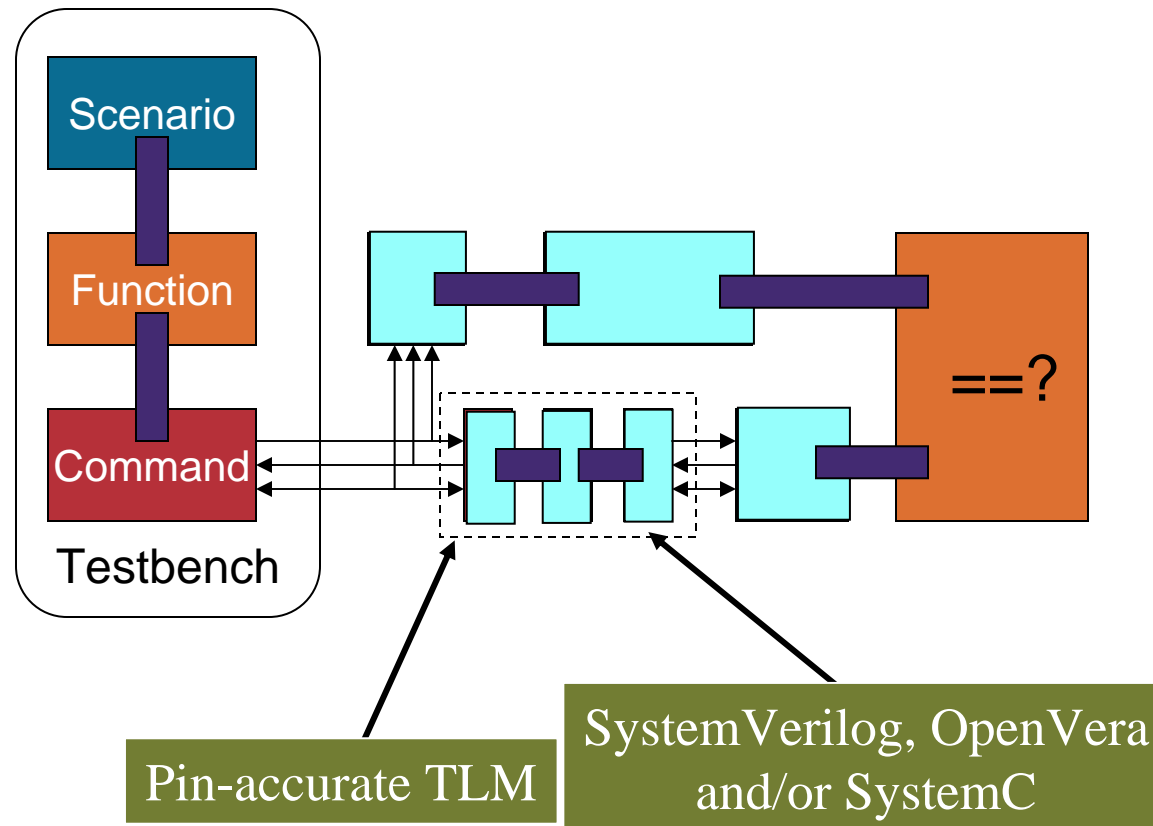
# Verification Environments



# Verification Environments

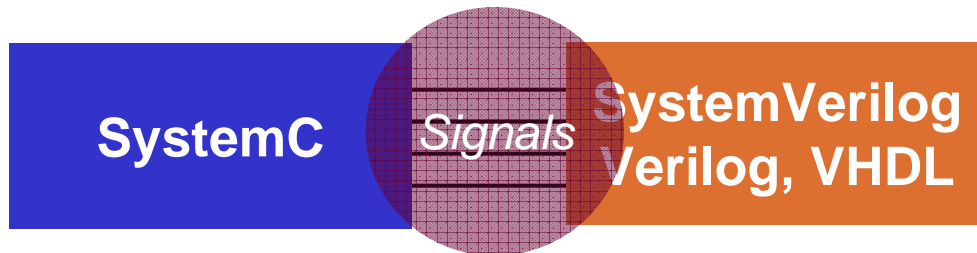


# Transaction-Level Models



# SC, SV, HDL interoperability

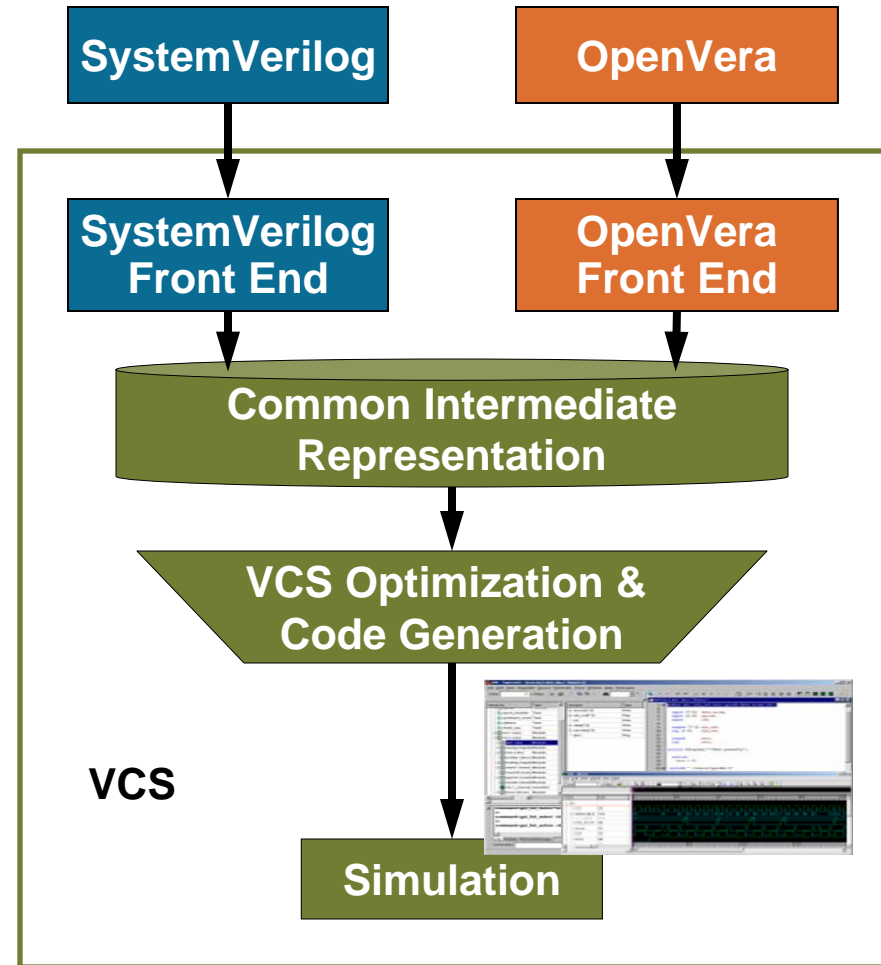
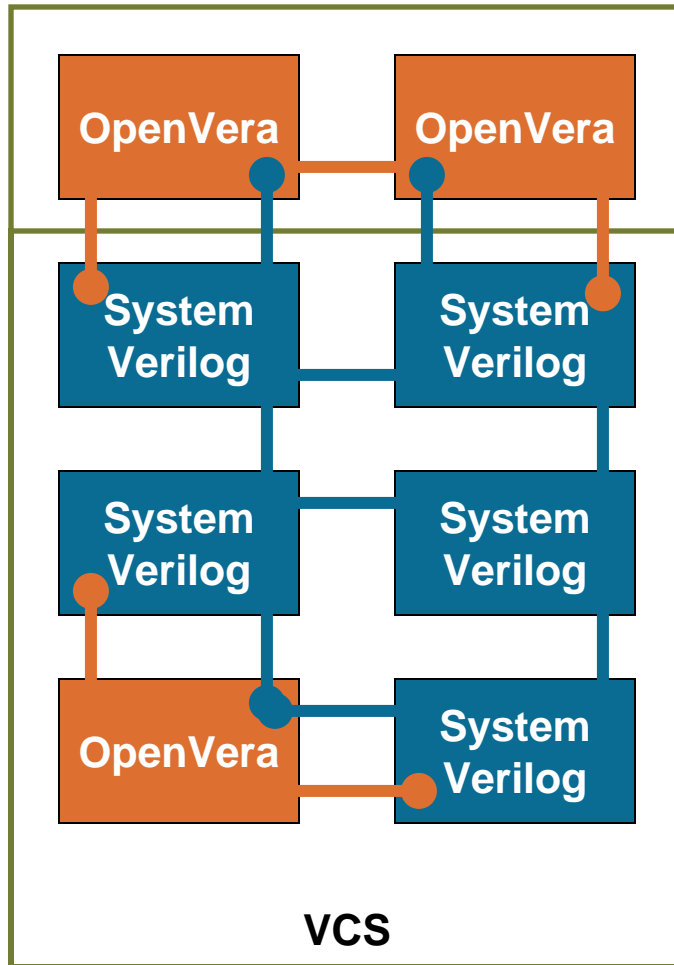
- Signal level interface



- Transaction-level interface



# OV-SV Interoperability in VCS®



# Summary

- Performance is a function of abstraction, not language
- Use right language for right application
- Use SystemVerilog or OpenVera for verification
- SystemVerilog and SystemC can be used for transaction-level modeling

**SYNOPSYS®**

**Predictable Success**