

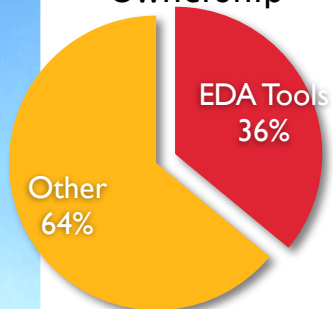
Update to



The Problem

Interoperability is Expensive

Cost of EDA Tool
Ownership



~\$2 support for
every \$1 EDA SW

Engineers waste 16% of
time on interoperability
and it adds 1.3 months
in cycle time

"We spend \$3-5 on
interoperability for every \$1
spent on EDA tools"
- Gadi Singer, Intel

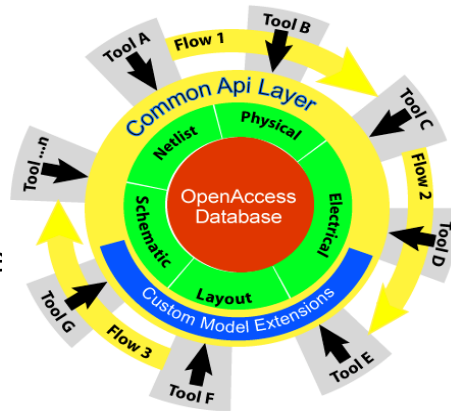
"The industry is spending \$7-9
billion on making tools work
together."
- Ron Collett, Collett Int'l



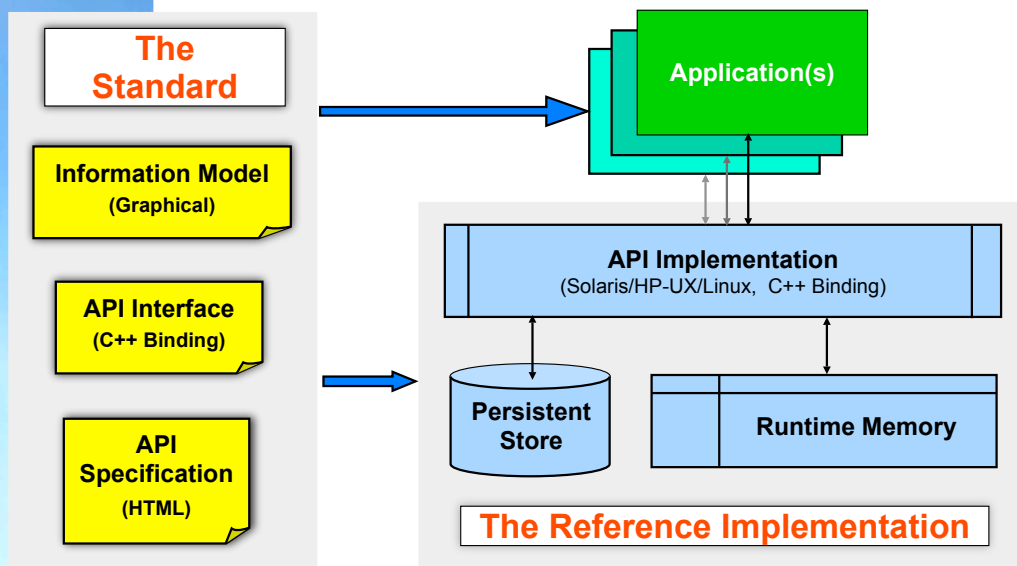
Objective

Establish an industry-wide design and manufacturing platform for communications and data exchange

- Solve the interoperability problem
- Accelerate innovations into market
- Provide freedom and choice to users



Components



OA Roadmap



OpenAccess Roadmap

This roadmap contains potential OpenAccess development projects suggested by the OpenAccess Change Team. The purpose of the roadmap is to capture some of the discussions regarding these projects and refine them.

The roadmap projects are currently organized into four categories:

- **Scheduled**
The project is assigned to a specific OpenAccess release. The integrator has planned to include this project in a release.
- **Resourced**
A company has allocated resources for implementing this capability. However, the project is not assigned to a specific OpenAccess release and is not guaranteed to be included in a release
- **Likely**
Multiple Change Team members are interested in this project and more detailed discussions of the project have occurred.
- **Possible**
Some member of the Change Team has expressed interest in this project.

Last Updated	Notes
12/3/04	First draft
	Added Incremental Tech to Likely Projects and added project description



Adoption

- Early adopters in OA Coalition/Community
 - ◆ LSI Logic, HP, and Cadence
- Active adoption by OA Coalition/Community members
 - ◆ AMI Semiconductor?, Freescale?, IBM, Philips?, Renesas?, Sun?
 - ◆ Atrenta, CiraNova, Gradient, Infineon, Invarium, In2Fab, MatrixOne, Mentor Graphics, Rio Design, Sagantec, Silicon Canvas, Silicon Navigator, Softjin, Synopsys?, VeriflC, Voom, and Cadence, and others,...
 - ◆ Several other vendors (Pyxis?, JEDAT, Zuken?,...) developing new capabilities
- Under active evaluation in OA Coalition/Community
 - ◆ AMD, Intel, Micron?, National Semiconductor?, SIINT?, STMicroelectronics?, SUN
- University interest in OA
 - ◆ Courses or research projects using OA - CMU, UCB, UCLA, UCSD, NCSU
 - ◆ Growing interest in Asia-Pac
 - ◆ OA Gear - Timing engine, GUI, Placement engine, ...

In production

end of year

Adoption Support

OpenAccess evolution

Si2.org hosts coalition, Change Team, working groups, related support services

OpenAccess training for tool

CD, Book, Si2 online course

Online searchable, access to documentation

Si2oaDebug simplifies debugging

OpenAccess implementation, tools, data

Adoption Support

OpenEDA website

Distribution point for code, specs & app notes

Trackers, forums to organize, archive and exchange information

Centralized repository for related areas: OMC, DTMC

OpenEDATools website

Enabler for community, university collaborative development

OpenAccess events

2 OpenAccess Conferences per year

Multiple panel sessions at external conferences

Press releases and articles in industry newsprint, e.g., EE Times

OpenAccess Tools Registry

Promotes OpenAccess-compatible applications

Progress and Plans

Recent Accomplishments

OpenAccess 2.2.4 released 01/17/2006

Training to SIINT & Keirex (Japan), AMD & SUN employees

Si2oaDebug v1.0 beta release completed 03/06/2006

Upgrade of Timing Constraints to OA 2.2.3 11/2005

Undergoing customer testing at AMD, LSI and Silicon Navigator

Next Milestones

Training materials and API Reference Manual in updated to 2.2.4

Release OpenAccess v2.2.5

Si2oaDebug production 04/11/2006

Self-running demo will be installed on website

OAC Contacts

For more information, visit:

www.si2.org

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