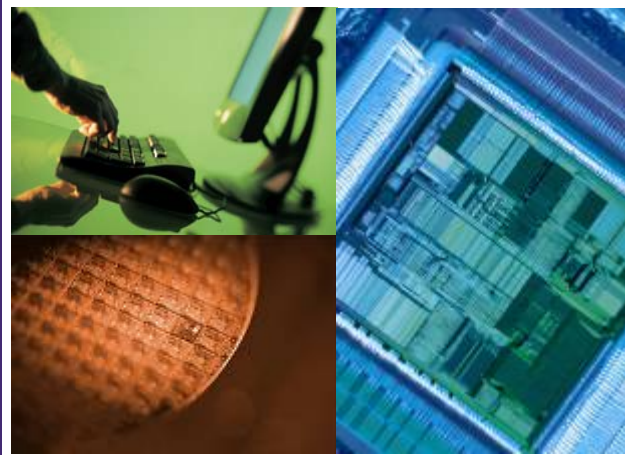


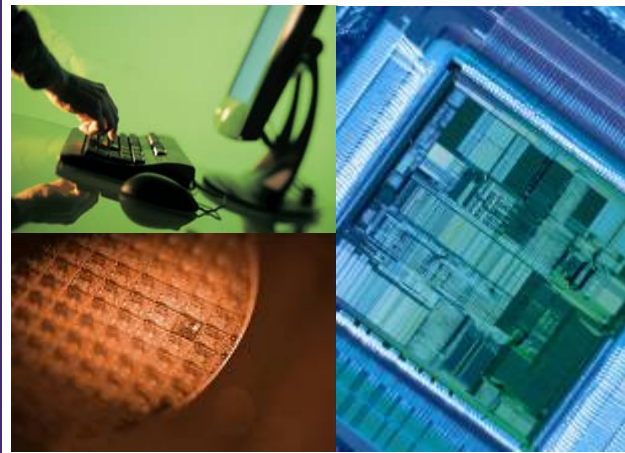
Welcome to the 17th EDA Interoperability Developers' Forum

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Welcome to the 17th EDA Interoperability Developers' Forum



Rich Goldman
Vice President
Strategic Market Development

SYNOPSYS[®]
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Who's Who at the Developers' Forum

Aldec, Altera Corp., Altos Design Automations, AMIS, Ansoft Corp, Apache Design Solutions , ARM, ASC, asdsa, Atoptech, Azuro, Bay Microsystems, Bell & Assoc., Blaze DFM, Blink Simulations, Blue Pearl Software, Broadcom, BTN, Cadence Design Systems, Chartered Semiconductor, ChipMD, CiraNova, Cisco Systems, Cypress Semiconductor, EDA Confidential, EDA Consortium, Engineering DataXpress, ESA INDIA PVT LTD, fangtek , Fujitsu Microelectronics America, Incentia Design, Intersil Corp, Jasper, JL Consulting, LaserResearch, Legend Design Technology, Library Technologies, LMC, LogicVision, LSI Logic Corp., Marvell Semi, Maxim Integrated Products, Mentor Graphics , Micro Magic, Micron Technology, Microsoft, Montalvo Systems, NoBug, Novas Software, O2Micro, Orbis Electronics, Pericom Semiconductor, Precision Software Development, Prexient Micro Devices, Project VeriPage, Prolific, purplevisiontechnologies, QCC, Qualcomm CDMA Technologies, Rambus, Real Communications, Real Intent, RLS Consulting, S3 Graphics, Silicon Canvas, Silicon Navigator, Silicon Valley Technical Institute, SiliconMap, Simucad, Simutest, SolutionSoft, Sonics, Sun Microsystems, Synplicity, Toshiba America , Transmeta , TSSI, Tuscany Design Automation, UMC, Verific Design Automation, Xilinx Virage Logic, Z Circuit Automation, Zenasis Technologies

Developers' Forum: The Stage for Interoperability Advancements



Developers' Forum: The Stage for Interoperability Advancements

- Launches 1st EDA Interoperability Developers' Forum
- Adds SDC format to Liberty (.lib & STAMP) in TAP-inSM licensing program



1998

Developers' Forum: The Stage for Interoperability Advancements

- Announces EDASpine
- Launches Open SystemC™ Initiative



1999

Developers' Forum: The Stage for Interoperability Advancements



2000

- Transitions TAP-in SDC & Liberty formats to open source
- Opens SURF (Secure User Research Facility) Lab
- Adds Physical library (PLIB) extensions to Liberty
- Debuts SDC open source parser
- Debuts Liberty open source parser
- Adds Scalable Polynomial Delay Modeling (SPDM) to Liberty

Developers' Forum: The Stage for Interoperability Advancements



2001

- Announces Vera® open source initiative
- Debuts Liberty open source screener
- Holds 1st OpenVera® Developer's Forum
- Launches OpenVera Catalyst Program
- Presents 1st Tenzing Norgay Interoperability Achievement Award

Developers' Forum: The Stage for Interoperability Advancements

- Extends TAP-in with SAIF open source format for power
- Releases OpenVera open source parser



2002

Developers' Forum: The Stage for Interoperability Advancements



2003

- Launches MAP-inSM program
- Holds 1st MilkywayTM Developers' Forum
- Adds Milkyway C-API to MAP-in program
- Announces SystemVerilog Catalyst program

Developers' Forum: The Stage for Interoperability Advancements



2004

- Announces Synopsys' corporate membership in IEEE-SA
- Debuts the SystemVerilog Developers' Forum
- Adds CCS modeling capabilities to Liberty
- Holds the Low Power Forum
- Presents EDA Consortium O.S. roadmap
- Discusses Software Piracy and Export Control

Developers' Forum: The Stage for Interoperability Advancements



2005

- Adds V-SDC open source format for equivalency checking to TAP-in
- Announces IEEE Std. 1800™-2005 (SystemVerilog) ratification
- Presents Verification Methodology Manual (VMM)
- Announces source-code licensing for SystemVerilog Verification Library

Still the Real Deal!



Still the Real Deal!



2006

- Plus prizes!
- Highlights industry initiatives and standards
 - CELUG
 - SPIRIT
 - SystemVerilog
 - TAP-in: Liberty & SDC
- Features relevant panel discussion:
“Open Analog: The Final Frontier”
- Presents in-depth technical sessions *(held this morning)*
 - CCS: An Industry Update
 - Using EDA Databases: Milkyway & OpenAccess
- Features the keynote presentation by Sunil Joshi, Sun Microsystems: “Opening the Doors to Hardware Innovation”
- Launches Si2 Liberty Technical Advisory Board (TAB)

TAP-in Update:

New Open Source SDC 1.6 Commands

- Adds 4 new constraints for multi-voltage and low power design
- Available Summer 2006
- Download latest documentation & parser:
www.synopsys.com/tapin
- Synopsys 2006.06 release will support SDC 1.6
- Note: SDC Parser supports all previously supported SDC formats
- Questions?: sdc@synopsys.com

Download Current Versions

- Synopsys Open Source Formats:

www.synopsys.com/partners/tapin

- Liberty 2005.10
- SAIF 2.0
- SDC 1.5
 - SDC 1.6 available in Q3, 2006
- OpenVera: www.open-vera.com
 - 1.4.2 Testbench LRM
 - 1.4.1 Assertions LRM

Download Current Versions

- Milkyway Access Program (MAP-in):
 - Milkyway Database Environment and C-API X-2005.09
www.synopsys.com/partners/mapin
- Open SystemC Initiative:
 - IEEE P1666 standard for SystemC approved in December 2006
 - IEEE 1666 LRM available in Q2, 2006
 - V2.1 Reference Simulator released & publicly available
 - Version 2.2 with full compatibility with IEEE 1666 under review
 - OSCI goal to transfer Transaction Level Modeling (TLM) 1.0 standard to IEEE in 2006

www.SystemC.org
www.eda.org/systemc

6th Annual Tenzing Norgay Interoperability Achievement Award

- Presented to an EDA company that
 - Surpasses common levels of interoperability
 - Contributes to overall industry advancement
 - Provides a new view of the future
 - Ensures customer success



*Tenzing Norgay
on Mt. Everest*

Tenzing Norgay Award

- Previous winners:

- CoWare (2001)
- Mentor Graphics (2002)
- Silicon Metrics (2003)
- Novas (2004)
- ARM (2005)

- 2006 Nominees

- Elgris Technologies
(formerly Electronic Tools
Company)
- Engineering DataXpress
- Library Technology
- Silicon Navigator
- Verific Design Automation
- Zenasis



And the winner is...



**...to be announced at the Synopsys
Interoperability Breakfast Event at DAC**

Wednesday, July 26, 2006

Presentations & Prizes...

- Presentations will be available for download from the web after June 1 at: www.synopsys.com/devforum/may2006/presentations
- Keep your raffle ticket to be eligible* for more prizes:
 - iPod® Nanos
 - Noise cancellation headset
 - *Verification Methodology Manual for SystemVerilog* by Janick Bergeron, Eduard Cerny, Alan Hunter and Andrew Nightingale, autographed by the authors.

**Must be present to win*

Mark Your Calendar!

18th EDA Interoperability Developers' Forum

- Thursday, November 9, 2006
- Located in Silicon Valley
- Invite your colleagues
- Want to speak?

See you then!

Enjoy your
17th EDA Interoperability
Developers' Forum