

Welcome to the
16th EDA Interoperability
Developers' Forum

sponsored by



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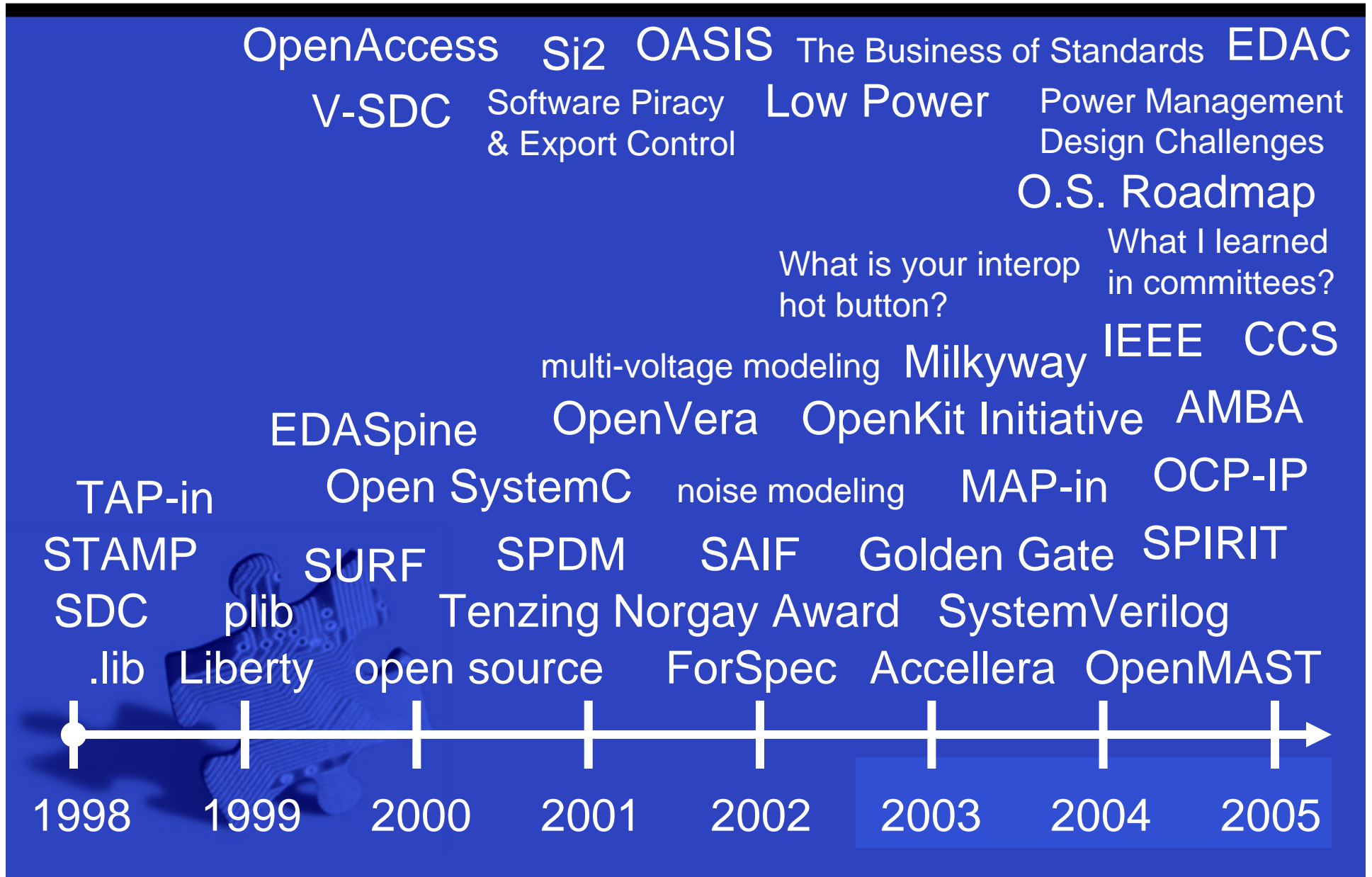
Rich Goldman
Vice President
Strategic Market Development
Synopsys, Inc.



November 9, 2005

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Expansion of Forum Topics



Top 6 Reasons You Come to the Forum

1. Industry issues & interoperable solutions
2. Keynote presentation
3. Networking with other attendees
4. Interested in specific topics of technical sessions
 - SystemVerilog, Databases, Libraries
5. Free Food!
6. Other*

* “My boss signed me up!”

What you've come to expect...

- **Interesting keynote presentation**
 - Today: Jim Solomon, Founder of Cadence
- **Industry initiatives and standards**
 - EDA Consortium
 - IEEE PA1800 SystemVerilog
 - OASIS
 - V-SDC for Equivalency Checking
- **In-depth technical sessions**
 - Moving to Open EDA Databases (this morning)
 - Innovations in Open-Source Liberty Library Modeling
 - Developing a SystemVerilog Ecosystem
- **Fun prizes**
- **Big news – Be sure to stay to hear the latest in Library Modeling and SystemVerilog!**

5th Annual Tenzing Norgay Interoperability Achievement Award



- Presented to a company that
 - Surpasses common levels of interoperability
 - Contributes to overall industry advancement
 - Provides a new view of the future
 - Ensures customer success

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And the winner is...



ARM®

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6th Tenzing Norgay Award

- Will be presented at DAC 2005 breakfast event
- Nominations are active for 3 years
- Call for nominations now until March 24th

<http://www.synopsys.com/tapin/tnorgay>



Mark Your Calendar!

17th EDA Interoperability Developers' Forum

- Thursday, May 11, 2006
- Located in Silicon Valley
- Invite your colleagues
- Want to speak?



See you then!

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A note about presentations...

- Presentations will be available for download from the web after November 23rd at:
www.synopsys.com/devforum/nov2005/presentations
- Hard-copies of the slides for the Liberty Library and SystemVerilog sessions will be distributed this afternoon
- Please plan to stay...plus enter the prize raffles* later in the day:
 - Apple iPod shuffle at end of General Session (~2PM)
 - Apple iPod® nanos and gift certificates to Palo Alto's Blue Chalk Café at end of Liberty and SystemVerilog sessions (~5:30PM)
 - *Verification Methodology Manuals for SystemVerilog* during the SystemVerilog session
- **Now, time for the 1st iPod shuffle raffle***

**Must be present to win.*

Enjoy your **16th** EDA
Interoperability Developers'
Forum



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