

OK initiative

it's OK to develop standards for custom design

**Empowering custom IC design by
cleaning up the mess underneath**

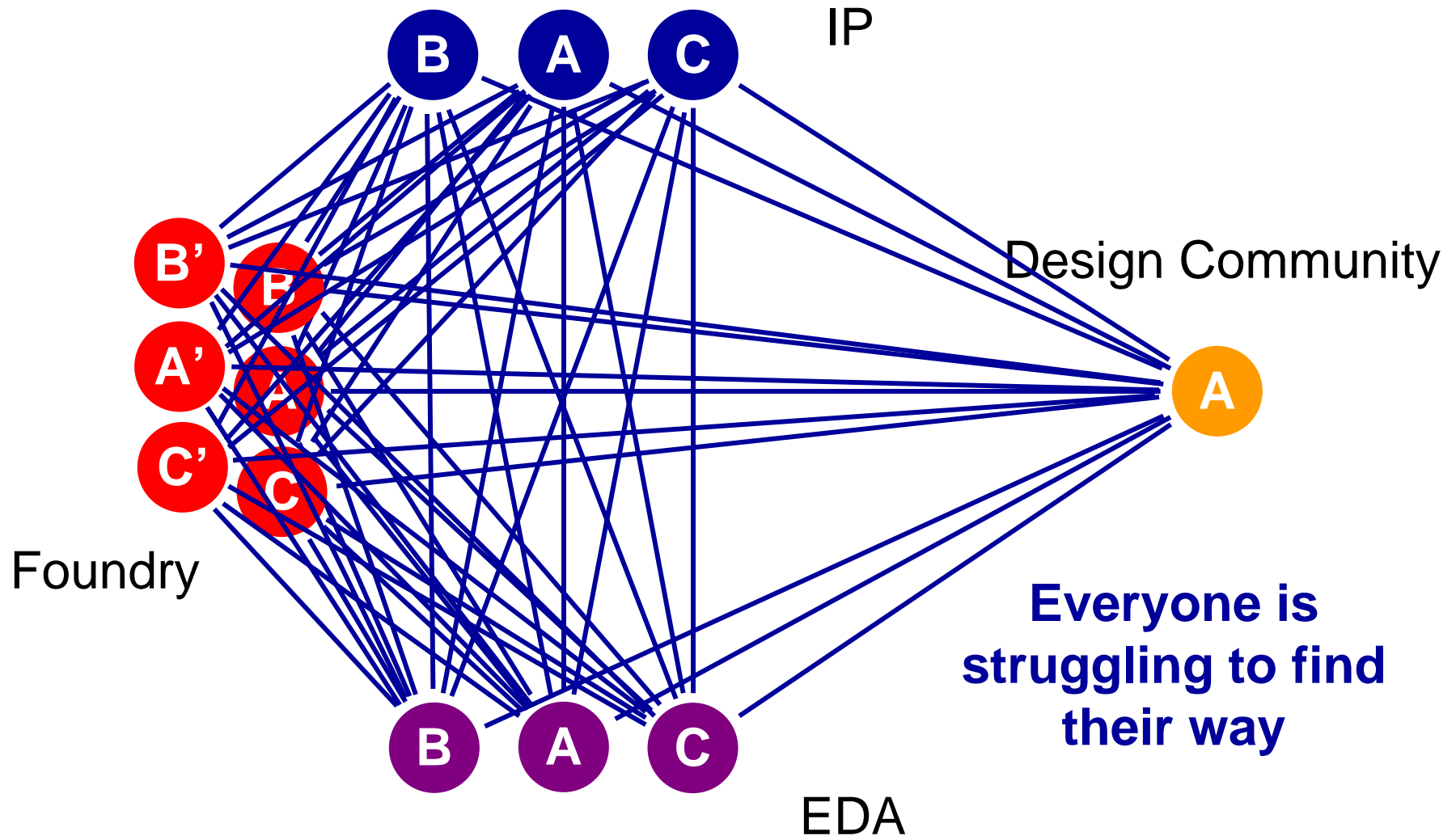
Interoperability 2003

The OpenKit Initiative

How about
Interoperability
at the
Transistor Level?
OK!

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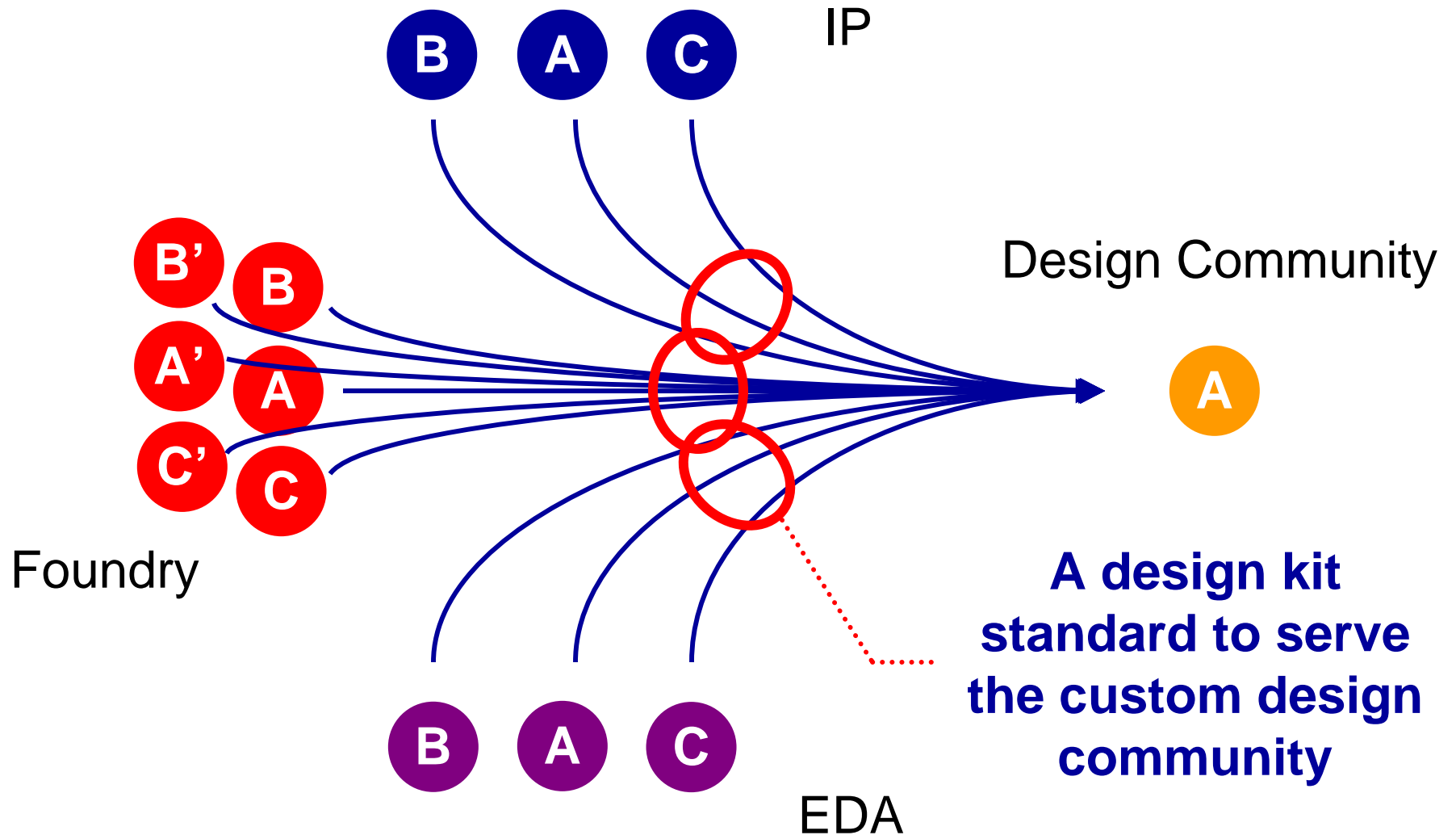
The 30,000 Foot View: A Jungle



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The 30,000 Foot View: Future



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Silicon World: 5 Basic Components

- **Components**
 - Transistor
 - Diode
 - Res
 - Cap
 - Ind
- **But... Standards?**
 - GDSII?
 - HSPICE?
 - Artist?
 - Calibre?
 - Virtuoso?
 - Cadence PDK?
- **Lack of standards at elemental level**
 - Creates huge inefficiencies
 - Inhibits progress in the custom design world

“We’re proposing a stronger foundation”

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OK Objective

Put a Strong Foundation to the IC Design Process

**Reduce Redundant Work by Multiple Companies
and the Inefficiencies and Nuisances**

**Work in Areas of Minimal Competition Between
Vendors**

**Continue Standardization Efforts
That are helping the industry**

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Things to work on

- Design Function, Nomenclature and categories
- Design kit elements and formats
- Tool & technology fit
- Design type and applications

- Management of all this stuff
 - Ownership
 - Qualification
 - Revision Control
 - Distribution

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Who Cares?

- **Designer community**
- **Library & IP developers**
- **Internal DA groups**
- **EDA tool providers**
- **Foundries**

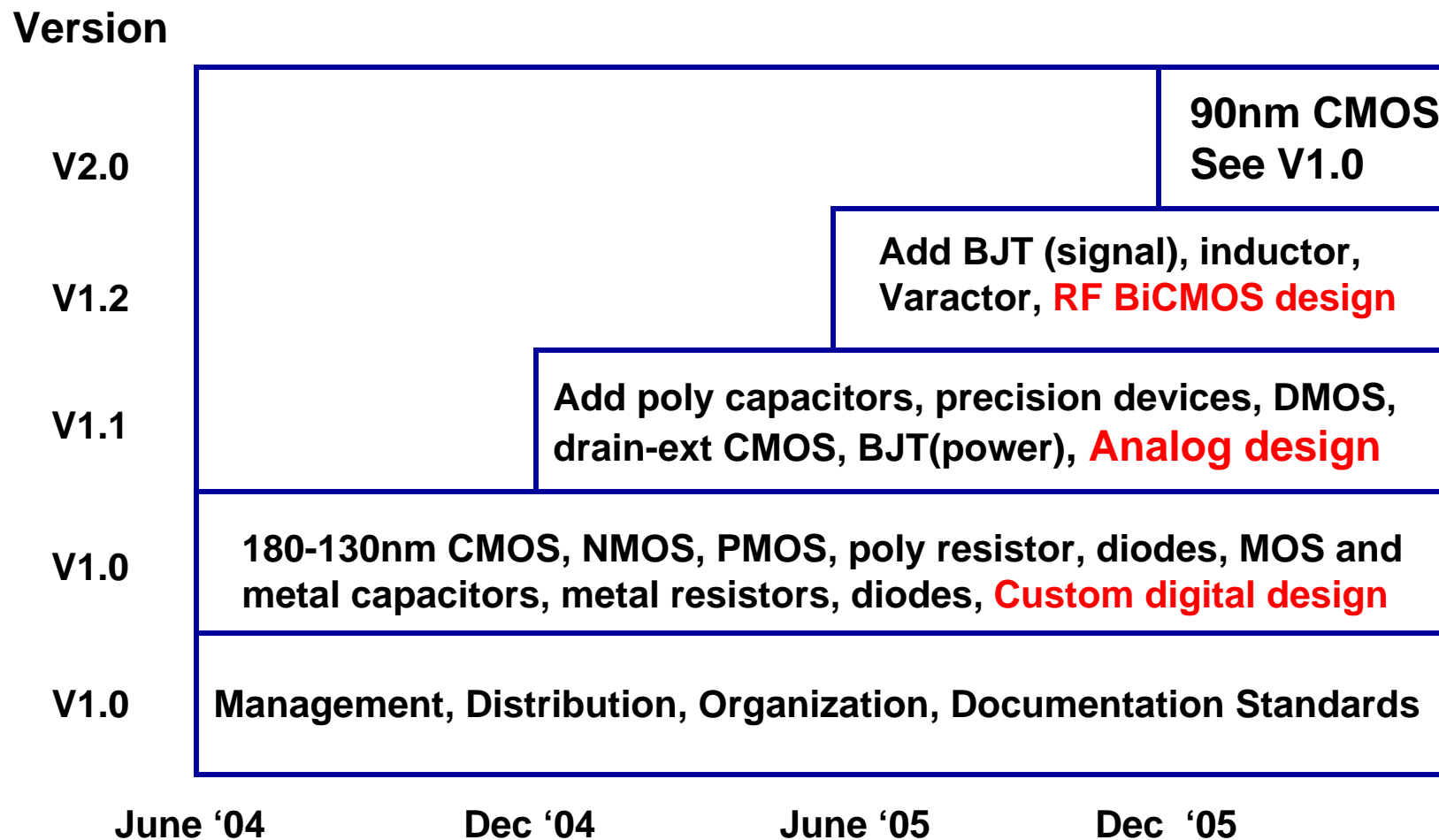
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OK Participating Companies

- 1st Silicon
- ADA
- AMIS
- Artisan
- AWR
- Barcelona
- Cadence
- Chartered
- Cirra Nova
- Conexant
- HPL
- IC Methods
- Mentor
- Motorola
- National
- NEC
- Philips
- Qualcomm
- Sagantec
- Silterra
- Silvaco
- Skyworks
- Sony
- Synopsys
- Texas Instruments
- TSMC
- Virage Logic

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OpenKit Roadmap thru '05



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Time To Take Action

- Time to find a better way
- Time to stop subscribing to the old beliefs
- Time to establish a foundation that everyone can build on
- Time to empower the Custom IC designer

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Q&A and Invitation

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