



→ OPENVERA

11th EDA Interoperability Developers' Forum

OpenVera Session

Jim Watts
OpenVera Program Manager
Synopsys, Inc.



Agenda

- **OpenVera Program Intro**
 - **Jim Watts; Synopsys**
- **OpenVera Assertions LRM Update for Portability**
 - **Bruce Greene; Synopsys**
- **Industrial Experience in PCI Express OVA Checker IP Development**
 - **Ashok Natarajan, Ram Gopalan; NetSys**
- **Debug for Assertion-Based Verification: Extending Novas' debug systems to support OpenVera Assertions**
 - **Bassam Tabbara; Novas**
- **Implementing High Performance OpenVera Assertions in Mixed-HDL Simulation**
 - **Mirek Forczek; Aldec**

OpenVera Momentum Continues

- OpenVera v1.3 TestBench and Assertions Released
 - **Download NOW:** <http://www.open-vera.com>
- Community Support Broadens
 - **Aldec - Riviera**
 - **Novas – Verdi**
 - **TNI-Valiosys – imPROVE-HDL**
- Verification and Assertion IP Portfolio Expands
 - **ControlNet – TCP/IP Verification IP**
 - **NetSys – PCI Express, PCMCIA, UART, Parallel 1284 OVA IP**
 - **Silicon Interfaces – Bluetooth, USB2.0 OVA IP**
- OpenVera Parser Now in Beta
 - **Interested in Testing?**
 - **Send email to:** feedback@open-vera.com

6th OpenVera Developers Forum

- **October 2003**
- **Located in Silicon Valley**
- **Mark your calendar**
- **Invite your colleagues**
- **Want to speak?**

<http://www.open-vera.com>

